

# Examination Winter Term 2015/2016 Communication Systems and Protocols



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## Communication Systems and Protocols

Date: 15.02.2016

Name:

Matr. ID:

ID:

Lecture Hall:

Seat:

## Prerequisites for the examination

### Aids:

- Additional aids allowed are:
  - Pocket calculator
  - One DIN-A4 sheet of handwritten notes
- Use only indelible ink - use of pencils and red ink is prohibited.
- Other material than that mentioned above, is strictly forbidden. This includes any type of communication to other people.

### Duration of the examination:

The exam duration is 120 minutes.

### Examination documents:

The examination comprises 29 pages (including title page, 7 blocks of tasks).

Answers may be given in English or German. A mix of language within a single (sub)-task is not allowed.

**Please check your matriculation number and id on every page before processing the tasks.**

In your solution mark clearly which part of the task you are solving. Do not write on the backside of the solution sheets. If additional paper is needed ask the examination supervisor.

### End of Exam:

You will not be allowed to hand in your examination and leave the lecture hall in the last 30 minutes of the examination. At the end of the examination: Stay at your seat and put all sheets (including this title page) into the envelope. Only sheets in the envelope will be corrected. We will collect the examination.

	Page	≈ Pts. [%]	Points
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Task 6: Practical Aspects of Communication Systems	21	12	<b>14</b>
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			Σ <b>111</b>

# Task 1: Physical Basics

## Task 1.1: TTL-Logic

A) Insert the logic level (HIGH, LOW) of the output and the state of the transistors (open, closed) into the table according to the input configuration  $x_1$  and  $x_2$  at the standard TTL output driver.

2

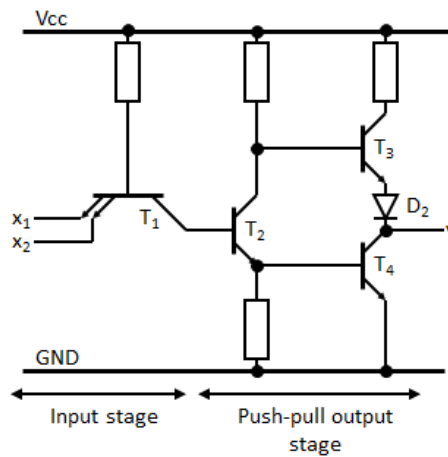


Figure 1.1:

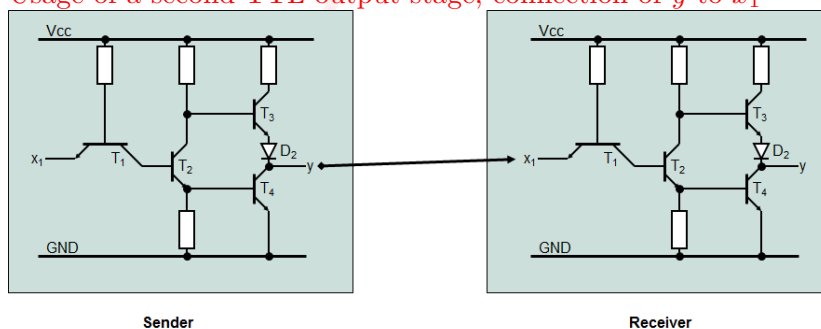
$x_1$	$x_2$	$T_1$	$T_2$	$T_3$	$T_4$	$y$
0	0	OPEN	CLOSED	OPEN	CLOSED	H
0	1	OPEN	CLOSED	OPEN	CLOSED	H
1	0	OPEN	CLOSED	OPEN	CLOSED	H
1	1	CLOSED	OPEN	CLOSED	OPEN	L

-0.5pt per wrong cell,  
consider consequential  
errors

B) How would a transmission setup with TTL technology look like? (Drawing is not necessary, but when using a description, it has to be clear and distinct)

1

Usage of a second TTL output stage, connection of  $y$  to  $x_1$



1pt for description or  
drawing, simplified  
drawing would be  
sufficient

C) List two advantages when using TTL drivers.

1

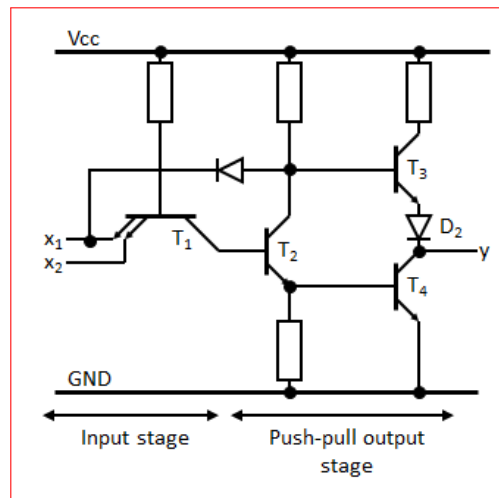
High currents are possible;

0.5pt per advantage

Valid HIGH and LOW areas are wider at the input due to possible voltage drops on the lines.

D) How would it be possible to overcome the disadvantage of possible short circuits of a TTL driver? Which part of the TTL driver needs to be modified? Modify the drawing to get the solution and describe the purpose of the adjustments made.

2



T1 needs an enable input. Additionally a diode in reverse direction is needed between enable and collector of T2.

1pt for correct drawing,  
0.5pt for description of  
enable, 0.5pt for  
description of diode.

## Task 1.2: Differential Signals

A) How could differential signal generation be realized?

1

Emitter Coupled Logic (ECL) (with twisted lines)

B) What are the advantages for differential signal transmission? Name two.

1

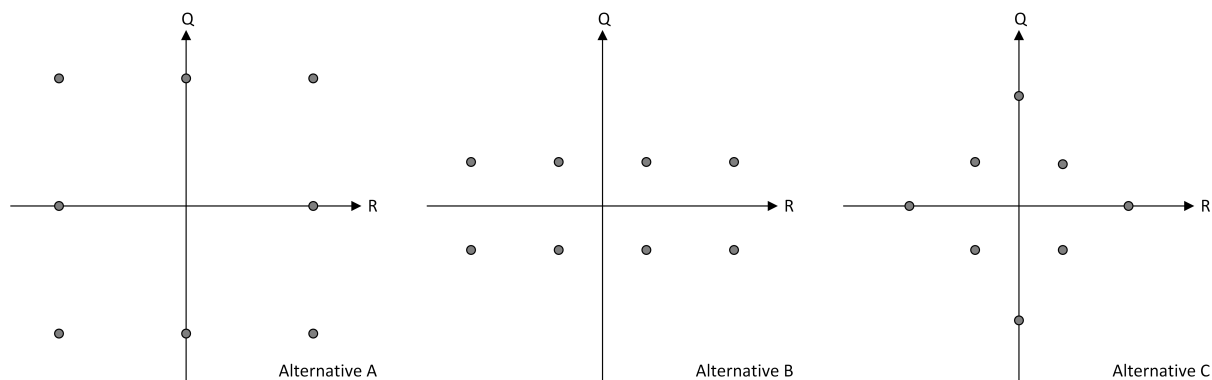
Higher speed since transistors don't go into saturation (ECL)

0.5pt per correct advantage

Inherent compensation of disturbances, noise pulse on both lines and therefore not visible in the differential signal.

### Task 1.3: Modulation

Now consider the following constellation diagrams for 8-QAM. All diagrams are drawn with the same scaling of the axes.



A) If you had to realize a communication system using QAM modulation, which alternative would you choose? Give reasons for your decision

2

Alternative A could be chosen because it provides the largest distance between the individual points. Therefore it provides the best resilience against disturbances.

2pt for solution with reasonable explanation. No points for simple answer without explanation. Other solutions can also be correct with proper explanation.

B) Briefly describe PSK modulation and give one advantage.

1

Phase shift keying: Discrete change of the phase of the carrier signal depending on the value to be sent. Phase changes can be detected easily thus providing simple and save demodulation

0.5pt for correct description of modulation scheme, 0.5pt for advantage

### Task 1.4: Channel capacity, Bandwidth

A digital transmission system with a bandwidth of  $B = 1,5 * 10^6 Hz$  has a channel capacity of  $C = 5 Mbit/s$  (according to Shannon).

A) What is the minimum for the signal to noise ratio (SNR) in dB?

2

$$C = B * \log_2(1 + S/N)$$

$$S/N = 2^{(C/B)} - 1 = 2^{(5Mbit/s/1,5*10^6 Hz)} = 9,079$$

$$SNR = 10 * \lg(2^{(C/B)} - 1) = 9,58dB$$

0.5pt for formula, 1.5pt for correct result, -0.5pt if result not in dB

B) Give the definition for the Cut-Off-Frequency:

1

Frequency at which the signal amplitude has dropped by 3db compared to the output value.

### Task 1.5: Signal Conversion

A) When converting analog signals into digital signals, what has to be considered in order to be able to achieve an unambiguous reconstruction of the signal (name and formula)?

1

Nyquist-Shannon sampling theorem:  $f_{sample} \geq 2 * f_{max}$

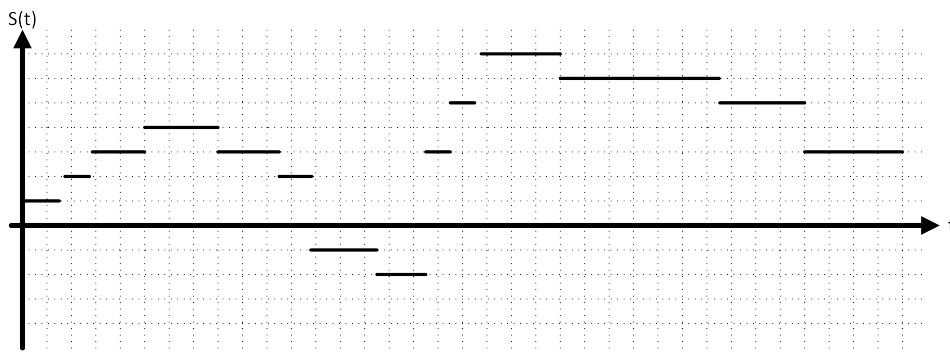
B) One can distinguish four different classes of signal. What are the parameters that are changed to form these classes? Give the combination of parameters that are characteristic for each class.

2

The important parameters are signal value and time. Both can either be continuous or discrete. The four different classes are:

- value-continuous, time-continuous
- value-continuous, time-discrete
- value-discrete, time-continuous
- value-discrete, time-discrete

C) Which signal class does the following signal belong to? Briefly describe where this type of signal can be used.



Signal class name: Time-continuous and value-discrete

Signal usage: Transmission of digital data, e.g. in computer bus systems. As the sampling timepoint is not exactly known before, the signal has to be sent continuously. As digital data is to be transported, only discrete values are required.

## Task 2: Wiring

### Task 2.1: General Questions

A) What is a symmetric line? Name one disadvantage of symmetric signaling.

1

Transmission of each signal over dedicated signal paths using inverse signal voltages **0.5pt for description**  
**0.5pt for example**

One pair of cables are needed for one signal → no reduction is possible, additional overhead for second voltage source and differential receiver.

B) How does the wire length affect the wave impedance  $Z_W$  in a lossless case?

1

It doesn't affect the wave impedance.

C) Name four causes for distortions of real data signals.

1

Noise, bad signal edges, glitches, cross talk, reflections, bad GND, bandwidth issues, (cosmic) Radiation, magnetic/capacitive distortions **0.5p per two correct causes**

D) Name the four different possible cases of the reflection factor  $r$  and describe shortly their mechanical analogue.

4

- $r = 1$  loose end
- $r = -1$  solid end
- $r = 0$  coupling joint
- $-1 < r < 1$  coupling joint

**0.5p for each case**  
**0.5p for each correct analogue**

## Task 2.2: Reflection on wires

You have found a transmission link in the basement and want to find out the wave impedance. With the setup given in Figure 2.2 you make the measurements that can be seen in Figure 2.2. The signal source  $U_S$  is stuck at an unknown output voltage and has an internal resistance of  $33\Omega$ . The termination resistance is  $R_T = 200\Omega$ . You can assume that the DC resistance is zero. When using numbers from Figure 2.2, only use one decimal place and only use values where the voltage is mostly constant.

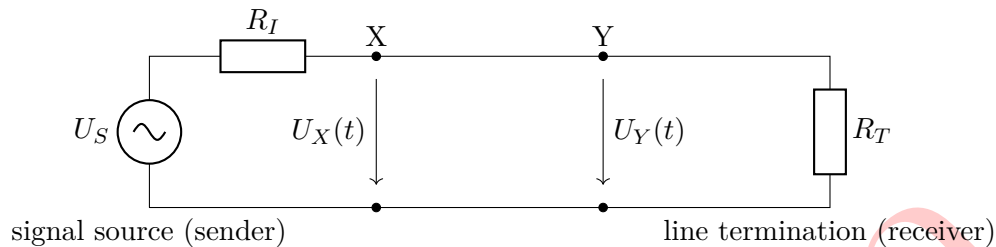


Figure 2.1: Test setup

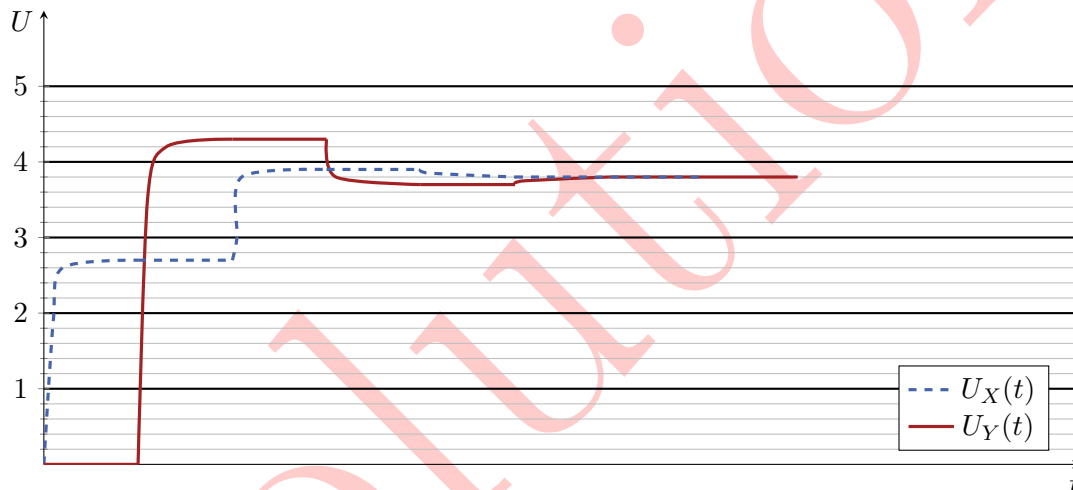


Figure 2.2: Measurement

A) How would you divide the timeline? Explain and mark at least four points on the timeline

1

$t_d$  (the propagation time of a wave on the wire) are quite distinct in the figure as the voltages on X and Y change abruptly. **0.5p  $t_d$  explained**  
**0.5p two points are marked**



B) Without calculation, make a quantitative statement about the reflection factors at the start and at the end.

1

$U_y$  overshoots  $U_\infty \rightarrow r$  at end positive,  
value goes down  $\rightarrow r$  at start is negative

0.5p for estimation  
0.5p for reasoning

C) Calculate the wave impedance (characteristic impedance)  $Z_W$  and the reflection factors at the start and at the end.

4

At point Y: forward running wave = 2,7V  
reflected wave = 1,6V  
 $r_e = \frac{1,6V}{2,7V} = 0,59$

At point X: forward running wave = -1,6V  
reflected wave = 0,4V  
 $r_s = \frac{0,4V}{-1,6V} = -0,25$

1p correct values from figure  
1p for  $Z_W$   
1p for each reflection factor

$$r_s = \frac{R_I - Z_W}{R_I + Z_W} \Leftrightarrow Z_W = \frac{R_I - r_s R_I}{r_s + 1} = \frac{33\Omega + 8,25\Omega}{0,75} = 55\Omega$$

D) Calculate the internal sender voltage  $U_s$

1

$U_X(\infty) = 3.8V$  from Figure

0.5p formula  
0.5p correct value

$$U_X(\infty) = U_S \frac{R_T}{R_T + R_I} \Leftrightarrow U_S = U_X(\infty) \frac{R_T + R_I}{R_T} = 3.8V \frac{200\Omega + 33\Omega}{200\Omega} = 4,427V$$

## Task 3: Data Transmission

### Task 3.1: Line Codes

A) Explain the Manchester Code encoding schemes and name one general application where it is applied.

2

The data are represented not by logic 1 or 0, but with line transitions.

1p for Explanation

1p for example (point only given together with explanation)

- A logic 0 is represented by a transition from high to low, and a logic 1 is represented by a transition from low to high
- The coding occurs on every falling edge of the clock

applications: Ethernet, ASI

B) Draw the digital signals for the bit string 010 101 111 000 011 using each of the NRZ, Manchester, and differential Manchester digital encoding schemes. Use figure 3.1.

3

1p for each correct line code

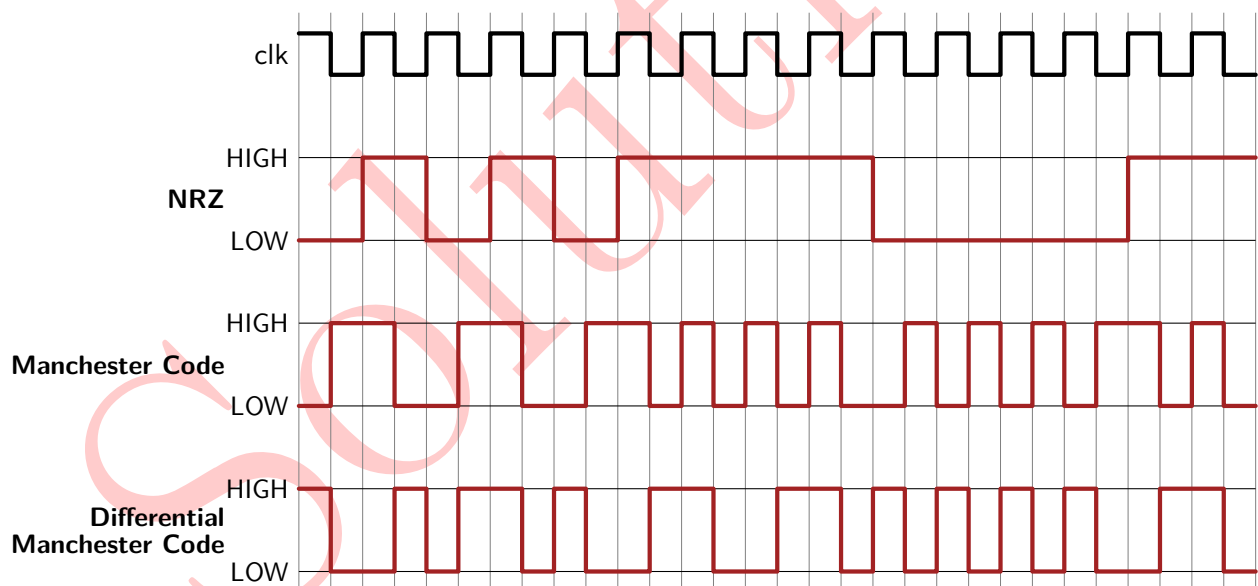


Figure 3.1: Line codes

**Task 3.2: CDMA**

A) What are the requirements for spreading codes used by CDMA?

**1**

The spectrum of the spread data function shall look like white noise

**0.5p for each**

Spreading functions have to be orthogonal (orthogonal means that the inner product of two functions equals to 0)

**requirement**

Sender node	Function
A	(+1, +1, -1, -1, +1, +1, -1, -1)
B	(+1, +1, +1, +1, -1, +1, -1, +1)
C	(+1, +1, -1, -1, -1, -1, +1, +1)

Table 3.1: Functions for sender nodes

B) Table 3.1 shows the functions of several sender nodes. Show that these functions fulfill the requirements and can be used to transmit data using CDMA.

**3**

requirement: functions have to be orthogonal  $\rightarrow$  inner product of two functions equals to 0)

function node A is orthogonal to function node B:

$$(+1, +1, -1, -1, +1, +1, -1, -1)^T \cdot (+1, +1, +1, +1, -1, +1, -1, +1)^T = 0$$

function node A is orthogonal to function node C:

$$(+1, +1, -1, -1, +1, +1, -1, -1)^T \cdot (+1, +1, -1, -1, -1, -1, +1, +1)^T = 0$$

function node B is orthogonal to function node C:

$$(+1, +1, +1, +1, -1, +1, -1, +1)^T \cdot (+1, +1, -1, -1, -1, -1, +1, +1)^T = 0$$

C) An additional node D should also be able to send data at the same time. Find another function for node D and show that your function is valid.

**2**

A valid solution: (+1, -1, +1, -1, +1, +1, +1, +1)

$$(+1, -1, +1, -1, +1, +1, +1, +1)^T \cdot (+1, +1, -1, -1, +1, +1, -1, -1)^T = 0$$

$$(+1, -1, +1, -1, +1, +1, +1, +1)^T \cdot (+1, +1, +1, +1, -1, +1, -1, +1)^T = 0$$

$$(+1, -1, +1, -1, +1, +1, +1, +1)^T \cdot (+1, +1, -1, -1, -1, -1, +1, +1)^T = 0$$

**1p point correct  
function  
1p for prove**

### Task 3.3: Symbol Stuffing

You want to transmit formatted text but due to limitations of your transmission system you can only use the uppercase letters A-Z and whitespace. However, it should be possible to transmit italic, bold and strike-through text.

To achieve this, the command character “C” is used which denotes the beginning and the end of a command sequence. The commands are then applied to all following characters until the command sequence is repeated. If the character “C” is to be sent as part of the text, it therefore has to be escaped by doubling it at sender site. Available commands are “B” for bold text, “I” for italic text, “L” for lowercase letters and “S” for strike-through text.

A) Format the following text according to these rules:

2

This task is ~~stupid~~ COOL

TCLCHIS TASK IS CBSCSTUPIDCBSC CLICCCOOLCIC

1pt for correct starting  
and ending of  
commands  
0.5pt for correct  
escaping of “C”  
0.5pt for correct  
sentence

B) What could happen if you did not use commands with an additional separating command word?

1

Repetitions would be problematic

## Task 4: Error Protection

### Task 4.1: Error Detection

A) What is the general difference between Safety and Security in relation to errors?

1

- Security: Malicious errors caused by attackers
- Safety: Accidental errors

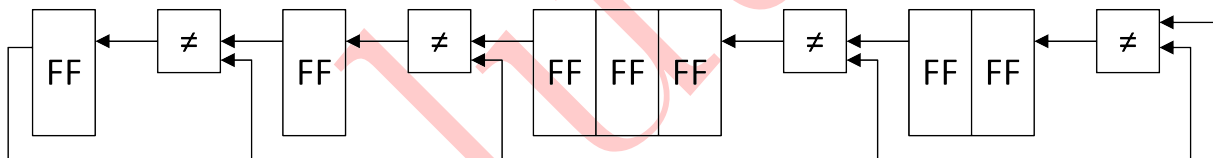
B) Name two properties of a good hash function for communication purposes.

1

- Different inputs should lead to different hash values (reduce number of collisions)
- Efficient calculation of hash possible
- Small change in input values  $\rightarrow$  large change in hash value

### Task 4.2: CRC-Calculation

To protect data transmissions, the given CRC scheme is implemented using linear feedback registers with XOR operations.



A) Determine the used generator polynomial.

1

$$x^7 + x^6 + x^5 + x^2 + 1$$

B) Calculate the data stream that will be transmitted if the following bit string is to be protected: **10101110**

3

$$\begin{array}{r}
 1010 \ 1110 \ 0000 \ 000 : 1110 \ 0101 \\
 \underline{1110 \ 0101} \\
 0100 \ 1011 \ 0 \\
 \underline{111 \ 0010 \ 1} \\
 011 \ 1001 \ 10 \\
 \underline{11 \ 1001 \ 01} \\
 00 \ 0000 \ 1100 \ 000
 \end{array}$$

2pt: calculation correct  
 0pt if systematic error  
 1pt if single calculation error  
 0pt if more than 1 calculation error  
 1pt for correct transmitted bitstream

Bit stream as it is transmitted: 1010 1110 1100 000

C) With a transmission system that uses CRC for error detection, a sender transmits the following bitstream: **1001 1101 0001**

3

Carry out the CRC error detection scheme of the receiver, assuming that the generator polynomial  $x^4 + x^1 + 1$  has been used to generate the checksum at the sender. What does the receiver conclude from the result?

$$\begin{array}{r}
 1001 \ 1101 \ 0001 : 10011 \\
 \underline{1001 \ 1} \\
 0000 \ 0101 \ 00 \\
 \underline{100 \ 11} \\
 001 \ 1101 \\
 \underline{1 \ 0011} \\
 0 \ 1110
 \end{array}$$

2pt: calculation correct  
 0pt if systematic error  
 1pt if single calculation error  
 0pt if more than 1 calculation error  
 1pt for receiver detects error in transmission

The receiver assumes that an error occurred during transmission.

### Task 4.3: Comparison of LRC and CRC

In Longitudinal Redundancy Check (LRC), a block of bits is organized in a table with rows and columns. Then the parity bit for each column is calculated and used to create a new row of an additional parity data word. After that, the newly calculated parity bits are attached to the original data and sent to the receiver.

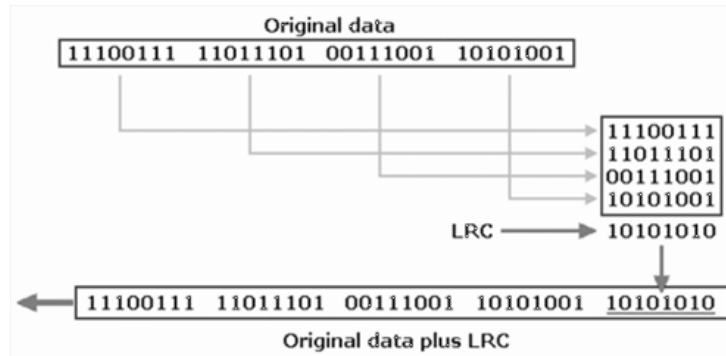


Figure 4.1: Example for LRC generation

- A) We now consider a transmission system that uses LRC with four datawords with six bits each. Even parity is applied. Using this system, the following bitstream was received: **1001 1000 1111 1011 0000 0100 0000 01**

Carry out the LRC error detection of the receiver. What does the receiver conclude from the result?

100 110  
001 111  
101 100  
000 100  
000 001

The receiver assumes error free transmission.

- B) Assume that there were disturbances during transmission so that the following bitstream is received instead of the original one: **1101 1001 1111 1011 0000 0100 0000 01**

Would these errors be detected by using LRC? What is the Hamming Distance (HD) of the LRC method?

Note: Hamming Distance (HD) is the lowest weight of any undetectable error. For example, HD=4 means that all 1, 2 and 3 bit errors can be detected.

No, the receiver will still assume that the transmission is error free. As soon as there are more than one error in a column, the error is not detectable any more. Therefore the Hamming Distance of LRC is 2

C) Could the errors in task B) be detected by using CRC with a well chosen CRC polynomial? If so, give the shortest possible CRC polynomial from the table below where CRC would guarantee detection of these errors. Explain your answer.

#	CRC Polynomial	Guaranteed HD	Up to max. data length (in bits)
1	CRC-3 ( $x^2 + 1$ )	HD=2	2048
2	CRC-4 ( $x^3 + 1$ )	HD=3	1
3	CRC-5 ( $x^4 + x^1$ )	HD=3	26
4	CRC-5 ( $x^4 + x^2 + 1$ )	HD=4	10
5	CRC-8 ( $x^7 + x^4 + x^3 + x^1 + 1$ )	HD=4	119
6	CRC-8 ( $x^7 + x^4 + x^3 + x^2$ )	HD=5	9

Yes, with a well chosen CRC polynomial, these errors would be detectable.

There are two CRC polynomials with  $HD > 2$  and max. data word length  $> 24$ .

CRC-5 (#3) and CRC-8 (#5).

As #3 is the shortest, it should be chosen.

D) Compare LRC against CRC on the following criteria:

- Error detection capabilities
- Implementation complexity

Which detection scheme would you prefer regarding these criteria? Explain your answer.

Error detection:

There are some CRC polynomials with  $HD > 2$  (and size  $< 6$  bits)  $\rightarrow$  CRC can detect more errors than LRC with given length of checksum.

Implementation:

CRC has a clever hardware implementation (shift registers)  $\rightarrow$  fast and cheap.

For LRC, the complete data plus LRC have to be stored and compared  $\rightarrow$  slower and more expensive For these criteria CRC seems to be more efficient.



# Task 5: Media Access

## Task 5.1: CSMA/CD

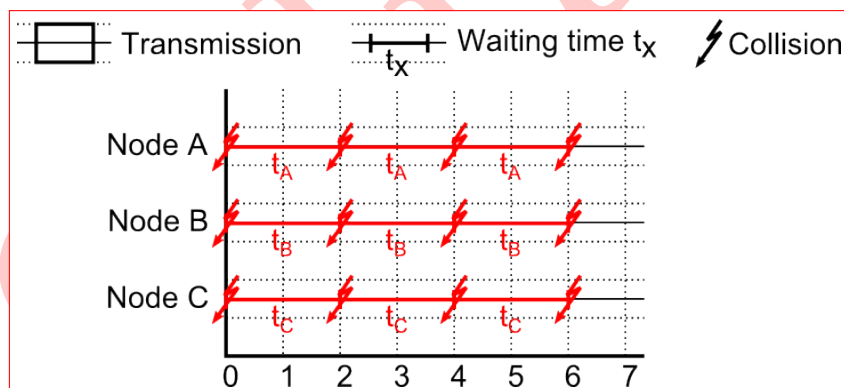
In this task we have a look at a bus system with arbitration that is derived from CSMA/CD. The following rules apply:

- All nodes want to send as many messages as possible. The length of each message is given in table 5.1
- A node is not allowed to send twice in a row. After each successful transmission it has to wait until another node has finished its transmission. The values of the assigned waiting times for each node are given in table 5.1.
- If a node willing to send detects that the bus is occupied it withdraws and waits for the time specified in table 5.1 (waiting time) until it will retry to transmit. Any ongoing transmission is not influenced.
- If two or more nodes want to start a transmission on the free bus at the same time there is a collision. All involved nodes withdraw from the bus and wait for the time given in table 5.1.

Node	Packet length	Waiting time
A	1	2
B	2	2
C	3	2

Table 5.1: Specification of nodes

- A) Fill in the signal sequence of the bus nodes, resulting from the specification as given above (use Figure 5.1). Mark waiting times and collisions that occur.



1p for correct solution

Figure 5.1: Signal sequence

B) Which problem occurs and how could it be solved?

1

Identical waiting time causes many collisions, transmitting is impossible. The waiting time has to be changed so that every node has a different waiting time.

+0.5p for collisions  
+0.5p for solution

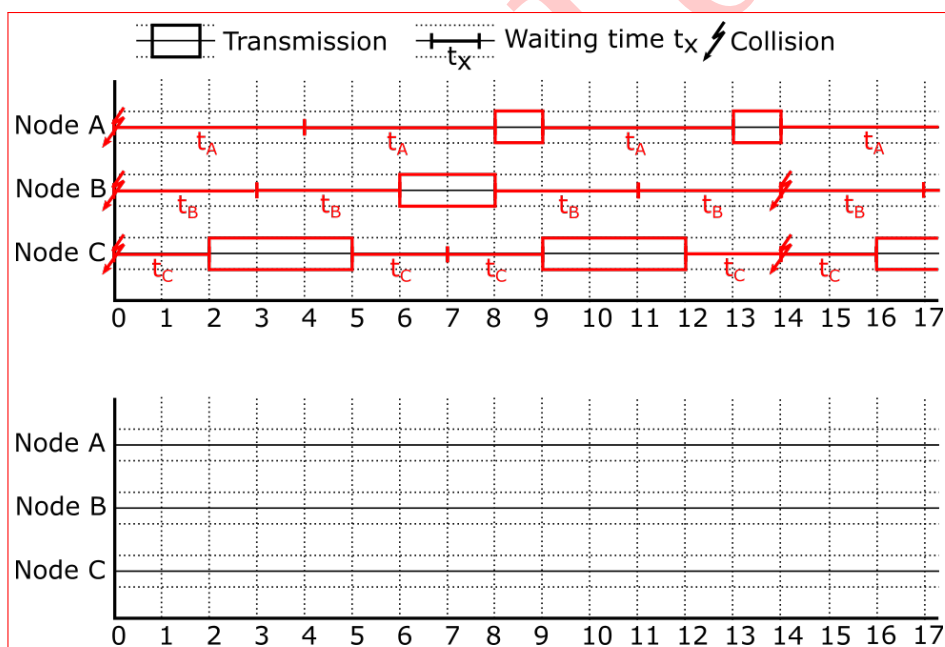
C) The packet length is unchanged and node C has the highest priority. Modify the waiting times so that all nodes have send data after nine clock cycles (use table 5.2). The waiting times should be as short as possible. Fill in the signal sequence of the bus nodes, resulting from the modified waiting times (use Figure 5.2). Mark waiting times and collisions that occur, label which graph should be evaluated with a cross.

5

Node	Packet length	Waiting time
A	1	4
B	2	3
C	3	2

+1 P for  $t_c=2$   
+1 P for  $t_b=3$   
+1 P for  $t_c=4$

Table 5.2: Modified waiting time



+1 P all send data until  $t=9$   
+1P correct solution

Figure 5.2: Signal sequence

## Task 5.2: CSMA/CA

A communication system comprises five communication nodes that use CSMA/CA as arbitration scheme. In order to transmit data a node transmits a dominant start bit (0) for synchronization purpose. After that a 5 bit message identifier followed and 10 bits of payload data is sent. The message identifiers are unique for each node and all data is sent MSB first. The bus has to cover a maximum distance of 500m.

A) Name two advantages and two disadvantages of CSMA/CA.

2

Advantages

0.5p for each advantage  
or disadvantage

- no collisions on the bus
- easy to prioritize because of individual identifiers
- partly real-time capable with additional rules
- efficient use of bandwidth

Disadvantages

- length of the bus and data transmission rate are limited because of simultaneity requirement

B) Which requirements have to be fulfilled in order to guaranty a faultless function of the system? What are the implications for the transmission rate?

1

The requirement of simultaneity has to be fulfilled.

+0.5P for Simultaneity

The signal propagation time  $t_s$  is much smaller compared to the digit length (bit time)  $t_b$ :

+0.5P for Transmission  
rate formula

$$\left[ t_s = \frac{l}{v} \right] \ll \left[ t_b = \frac{1}{TR} \right].$$

C) Calculate the maximum payload data rate of this bus. Assume a propagation time of 0.66c ( $c = 3 \cdot 10^8 \frac{m}{s}$ ).

2

Transmission rate:

$$\left[ t_s = \frac{l}{v} \right] \ll \left[ t_b = \frac{1}{TR} \right] \text{ with } l = 500m, v = 0.66 \cdot 3 \cdot 10^8 \frac{m}{s}$$

1P for transmission rate  
+1P for payload data  
rate

$$TR \ll \frac{v}{l} = \frac{0.66 \cdot 3 \cdot 10^8 \frac{m}{s}}{500m} = 396000 \frac{1}{s}$$

Start bit + 5 bit message identifier and 10 bits data:

$$\text{payload data rate} = \frac{10}{16} \cdot TR = 247500 \frac{1}{s}$$

D) Figure 5.3 shows a timing diagram for the bus system described above. Indicate the identifiers of the given nodes as far as possible (use Table 5.3). Mark undetermined identifiers bits as X!

2

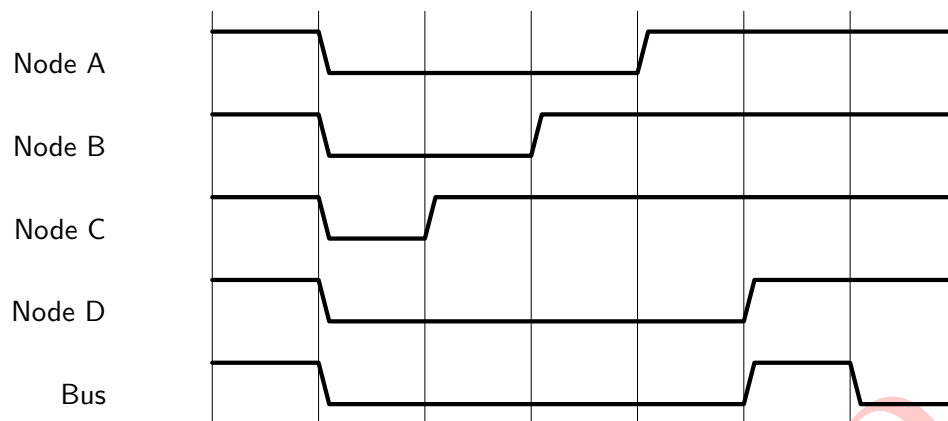


Figure 5.3: Bus Access

Node	Bit 0	Bit 1	Bit 2	Bit 3	Bit 4
A	0	0	1	X	X
B	0	1	X	X	X
C	1	X	X	X	X
D	0	0	0	1	1

-0.5pt for each correct line

Table 5.3: Identifiers of the nodes

E) Which node is granted exclusive access to the bus?

1

Node E (five nodes are mentioned in the task) is able to send data.

OR: No one of the four nodes A-D is allowed to send data.

# Task 6: Practical Aspects of Communication Systems

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## Task 6.1: General Questions

A student wants to transmit data over a very long distance. Because of budget reasons, a connection with one single wire has to be used for this transmission. The transmission should use Aloha with unipolar NRZI.

A) How can clock recovery be done for the above network?

1

Since a long line of '0's or '1's can be transmitted a bit stuffing is necessary to change the value frequently. A preamble can be used to synchronize the two clocks. 1p for correct answer either bit stuffing or preamble

---



---



---

B) The transmitted data will have variable length. Name two ways of determining the data field length within a transmission:

1

Length specification in length field of frame 0.5p for each correct answer  
Use of delimiter for data field

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C) What needs to be changed in order to make the network real time capable?

1

An appropriate protocol and media access have to be chosen. 1p for changing protocol or media access  
Examples: token passing at protocol level, FDMA, TDMA  
All access is controlled by one master

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## Task 6.2: Physical Layer of Customized Bus

A customized bus should code the raw data with Manchester. The voltage level on the bus is generated by an inductivity. This inductivity is driven by an open-collector that is connected to the output stage of the microcontroller. The transmission is initialized by a logical Zero (start-bit) and ended by a logical One (stop-bit).

A) What is the advantage of the induced voltage levels?

1

Sine-shaped pulses require lower bandwidth for transmission.

Data can be transmitted on power line

Also galvanical isolation might be possible

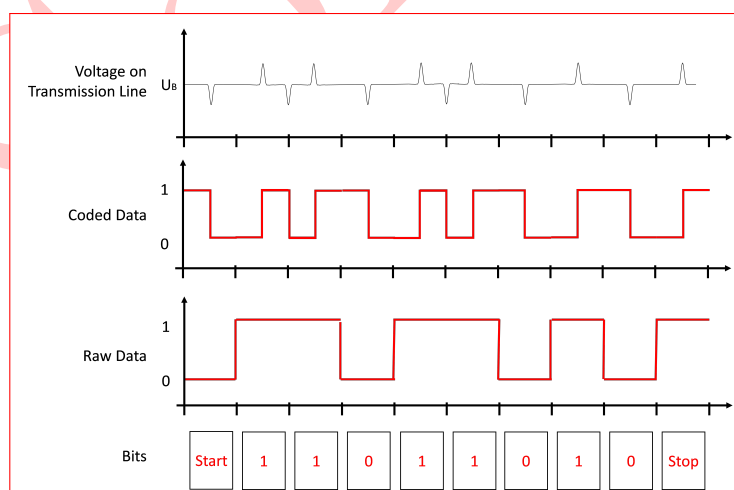
B) Can the clock be recovered within this system? Justify your answer. If clock recovery is not working give a possible solution.

1

Yes. Manchester is used → change of level for every bit.

C) Draw the Manchester coded and raw data (information) transmitted over the wire in Figure 6.1. Please write down the transmitted data. The figure shows the transmission of a complete frame.

3



1p for correct coded data  
1p for correct raw data  
1p for start and stop bit (it is OK, if first and last bit is empty)

Figure 6.1: Transmission on customized bus

D) How can data integrity be checked purely on physical layer? (Name two)

1

Check for start-bit and stop-bit

0.5p for each correct  
answer

Successive pulses must have different polarity

Between two clocks/pulses of a frame only one pulse is allowed to be missing

E) Because of license reasons Manchester coding cannot be used. Why can't differential Manchester be used for the system? Please name the Problem and a possible solution.

2

Differential Manchester is alternating, therefore last level depends on number of '0's and '1's. This means the stop bit ends not always at recessive level. For odd parity a second stop-bit or parity-bit has to be added.

1p for problem  
1p for possible solution

### Task 6.3: FireWire

A) Different FireWire structures were built during a student laboratory. During test phase you notice that not all FireWire systems are working correctly. Please state if the FireWire systems given below are working correct. Mark the roots, if the systems are correct. Give a reason, if the FireWire system is not working correctly.

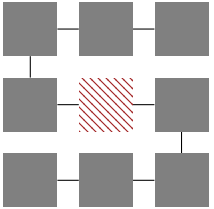
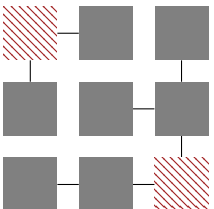
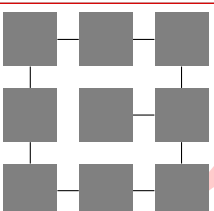
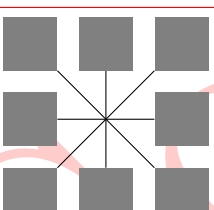
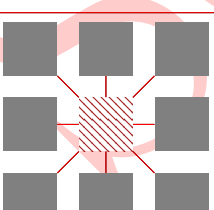
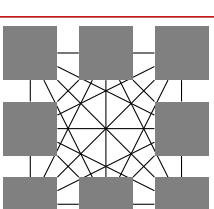
	Correct	Wrong	Reason
	x		
	x		
	x		Rings are not allowed in FireWire
	x		One output cannot have multiple connections
	x		
	x		Rings are not allowed in FireWire

Table 6.1: FireWire structures



Solution

# Task 7: Networks

## Task 7.1: General Questions

A) Your task is to decide on which type of connection to be used in a network consisting of components in need of predictable latencies. Justify your decision.

1

Circuit Switching, easier to guarantee latency

B) Your task is to decide on which type of connection to be used in a network consisting of components that mainly communicate by streaming data, thus in need of high and guaranteed throughput. Justify your decision.

1

Circuit Switching, easier to guarantee throughput

C) Name the three components of a network on chip node in the basic setup and their respective task.

2

Computing Unit: Runs an application or part of an application

Network Interface: Mediating between Computing Unit and Network

Routing Unit: Embedded intelligence that decides on the direction of the data

D) How do networks and busses differ from each other?

1

Bus: dedicated and fixed physical communication channel

Network: different and multiple communication channels are possible

There are other correct answers possible. 1pt is given for correct distinction.

## Task 7.2: Routing

Figure 7.1 shows a 4x4 meshed network with bidirectional links for wormhole packet-switching communication.

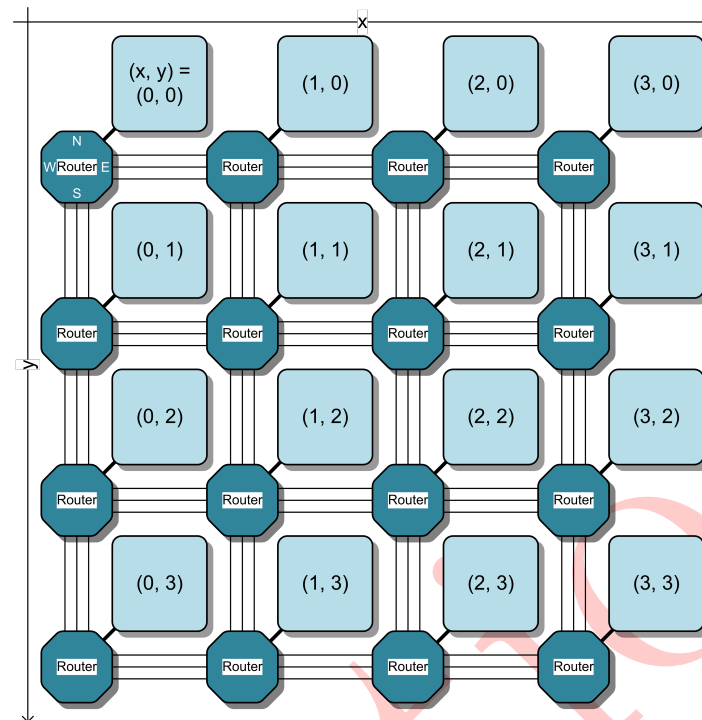


Figure 7.1: 4x4 meshed network

A) Which routers are passed by a packet sent from  $(x, y) = (1, 0)$  to  $(3, 3)$  using XY-Routing. Please provide the coordinates of the passed router in the order given by the transmission process.

1

$(1, 0), (2, 0), (3, 0), (3, 1), (3, 2), (3, 3)$

B) The routers  $(1, 0)$  and  $(2, 1)$  are experiencing heavy traffic towards their east port, such that packets have to wait before being forwarded. As an alternative “hot potato XY-Routing” is used. If a port is occupied the opposite dimension is used, so in case of X towards Y and in case of Y towards X. If no heavy traffic is present common XY Routing is used. Which routers are passed by a packet sent from  $(x, y) = (1, 0)$  to  $(3, 3)$  for that routing?

1

$(1, 0), (1, 1), (2, 1), (2, 2), (3, 2), (3, 3)$

C) Which classes of routing algorithms is hot potato XY-Routing associated with?

1

Adaptive Routing: Since Ports are used depending on Traffic in Routers

Non-Minimal Routing: New routes can lead to non-minimal detours

D) Describe two scenarios: one in which common XY Routing is preferable and one in which "hot potato XY Routing".

2

Balanced network traffic XY Routing will find the shortest Path

If heavy traffic is present at certain ports, hot potato XY can reduce the latency

E) Instead of XY-Routing, Flooding is considered for the given network. How many times is a packet forwarded when flooding is used, with router (1,0) being the origin and router (2,2) the destination?

2

$$4 \cdot 1 + 7 \cdot 2 + 3 \cdot 3 + 3 = 30$$

F) How many times is a packet forwarded by routers, using Flooding with a time to live of 2, when router (1,0) is the origin and router (2,2) the destination?

1

9

G) What is the minimal time to live for a packet sent by router (1,0) to reach router (2,2)?

1

3

## Task 7.3: Communication Models

This task focuses on Communication Models like the OSI reference model.

- A) The (notional) company “Simple Communications” has taken the position that layered communication models are unnecessary. Name one reason why they would fare better with a layered model and justify your answer.

1

Maintainability

Also other answers are possible if they show the advantages of layered models.

- B) What is the purpose of the presentation layer in the OSI reference model?

1

Correct data representation between Applications

A data transmission scenario is illustrated in figure 7.2. The latencies for data processing within each layer of the OSI reference model are shown in Table 7.1. The latencies can be assumed for all devices in this scenario.

#	Layer	Latency
1	Physical Layer	$1\mu s$
2	Data Link Layer	$10\mu s$
3	Network Layer	$100\mu s$
4	Transport Layer	$500\mu s$
5	Session Layer	$1ms$
6	Presentation Layer	$1.5ms$
7	Application Layer	$2.5ms$

Table 7.1: Latencies

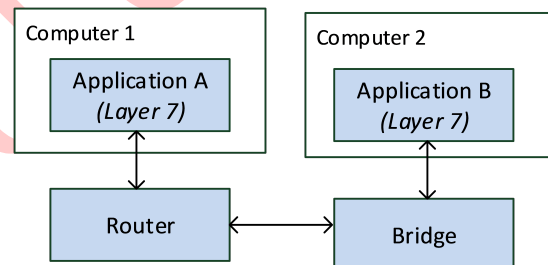


Figure 7.2: Data transmission scenario

- C) Calculate the communication latency for the router, bridge and for Computer 1. The payload size can be assumed as small. Thus, data transmission latency is assumed to be independent from the payload size.

3

$$\text{Router L3} = 2 * \text{Layer}[3] + 2 * \text{Layer}[2] + 2 * \text{Layer}[1] = 222\mu s$$

$$\text{Bridge L2} = 2 * \text{Layer}[2] + 2 * \text{Layer}[1] = 22\mu s$$

$$\begin{aligned} \text{Computer 1} &= 2 * \text{Layer}[7] + 2 * \text{Layer}[6] + 2 * \text{Layer}[5] + 2 * \text{Layer}[4] + 2 * \text{Layer}[3] + \\ &2 * \text{Layer}[2] + 2 * \text{Layer}[1] = 5ms + 3ms + 2ms + 1000\mu s + 222\mu s = 11222\mu s \end{aligned}$$